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1c720-0268 PTO
10/022551
12/17/01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLN. OF: SAEKI

FILED: December 17, 2001

FOR: CLOCK AND DATA RECOVERY CIRCUIT AND CLOCK CONTROL METHOD

DOCKET: NEC NEG-239

BOX PATENT APPLICATION
Assistant Commissioner for Patents
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

In connection with the above-entitled matter, Applicant hereby attaches U.S. Patent Office Form PTOL-1449, including copies of the prior art references listed therein. These references are discussed in the specification. The claims in the present application are believed to be patentably distinguished over these references.

This information disclosure statement is being made pursuant to the duty of disclosure imposed by law and formulated in 37 CFR 1.56(a). No representation is made that the information thus disclosed in fact constitute prior art or that it is the closest prior art, inasmuch as 37 CFR 1.56(a) relies on a materiality concept which depends on subjectivity.

In the event there are any fee deficiencies or additional fees are payable, please charge them (or credit any overpayment) to our Deposit Account No. 08-1391.

Respectfully submitted,



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INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Docket Number (Optional)

NEC NEG-239

Application Number

Applicant(s)

SAEKI

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December 17, 2001

Group Art Unit

1c720 U.S. PTO
10/02551
12/17/01

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		Fiedler, A., "A 1.0625GHz Transceiver with 2x-Oversampling and Transmit Signal Preemphasis", ISSCC 1997, pp 238-239
		Sidiropoulos, S. et al, "A semi-digital delay locked loop with unlimited phase shift capability and 0.008-400 MHz operating range", ISSCC 1997, pps 332-333

EXAMINER	DATE CONSIDERED
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*EXAMINER
 INITIAL

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

✓ "A2BParallel 1.25 Gb/s Interconnect I/O interface with Self Configurable Link and Plesiochronous Clocking", ISSCC 1999, pps 180-181

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